

GENERAL SLOT PATTERN

Day	8.30 – 9.25	9.30 – 10.25	10.35 – 11.30	11.35 – 12.30		2.30 – 3.55	4.00 – 5.25	5.30 – 6.55	7.00 – 8.25	
Monday	1A	2A	3A	4A	Lunch break between 12.30 to 2.00	8A	9A	12A	13A	
Tuesday	4B	1B	2B	3B		10A	11A	14A	15A	
Wednesday	7A	5A (9.30 – 10.55) -----L		6A (11.05 – 12.30) 5-----		X1	X2	X3	XC	XD
Thursday	3C	4C	1C	2C		8B	9B	12B	13B	
Friday	7B	6B (9.30 – 10.55) -----L		5B (11.05 – 12.30) 6-----		10B	11B	14B	15B	

SLOT 1	1A, 1B, 1C	One hour	SLOT 9	9A, 9B	One and half hour	Slot L1	3 hour slot
SLOT 2	2A, 2B, 2C	One hour	SLOT 10	10A, 10B	One and half hour	Slot L2	3 hour slot
SLOT 3	3A, 3B, 3C	One hour	SLOT 11	11A, 11B	One and half hour	Slot L3	3 hour slot
SLOT 4	4A, 4B, 4C	One hour	SLOT 12	12A, 12B	One and half hour	Slot L4	3 hour slot
SLOT 5	5A, 5B	One and half hour	SLOT 13	13A, 13B	One and half hour	Slot L5	3 hour slot
SLOT 6	6A, 6B	One and half hour	SLOT 14	14A, 14B	One and half hour	Slot L6	3 hour slot
SLOT 7	7A, 7B	Slot for quizzes	SLOT 15	15A, 15B	One and half hour	Slot LX	3 hour slot
SLOT 8	8A, 8B	One and half hour					

TIMETABLE FOR AUTUMN SEMESTER 2019-2020
V SEMESTER B.TECH (ELECTRICAL ENGINEERING)

Course No.	Course Name	Division	Department	Lecture		Instructor
				Slot	Venue	
EE 303	Control systems		EE, ME	7A, 7B	115	Prof. Ameer K Mulla
HS301/HS303	Introduction to Literature Philosophy		CSE, EE, ME	2A, 2B, 2C	HS 301 – 210 HS 303 - 203	Prof. Ridhima Tewari Prof. Jolly Thomas
CS 301	Computer Architecture		CSE, EE	1A, 1B, 1C	115	Prof. Rajshekar K
EE 306	Power Systems		EE	12A, 10A 4A, 4B, 4C	207	Prof. P. S. Nagendra Rao
EE 307	Introduction to probability		CSE, EE	3A, 3B, 3C	207	Prof. Bharath B N
EE 230	Analog Circuits Lab		EE	L6	Electronic Devices Lab	Prof. Naveen Kadayinti
EE 214	Digital Systems Lab	EE division	CSE, EE	LX	Electronic Devices Lab	Prof. Ruma Ghosh
CS 311	Computer Architecture Lab	EE division	CSE, EE	L1	Computer Lab (Ground Floor)	Prof. Rajshekar K.
EE 311	Electrical Machines and Power Electronics Lab		EE	L3, L4	Machines Lab	Prof. Satish Naik & Prof. Pratyasa Bhui
EE 303	Control Systems Lab	EE division	EE	14A (extended by half an hour)	DSP lab	Prof. Ameer K Mulla