

GENERAL SLOT PATTERN

Day	8.30 – 9.25	9.30 – 10.25	11.00 – 11.55	12.00 – 12.55		2.30 – 3.55	4.00 – 5.25	5.30 – 6.55	7.00 – 8.25	
Monday	1A	2A	3A	4A	Lunch break between 12.30 to 2.00	8A	9A	12A	13A	
Tuesday	4B	1B	2B	3B		10A	11A	14A	15A	
Wednesday	7A	5A (9.30 – 10.55)		6A (11.05 – 12.30)		X1	X2	X3	XC	XD
Thursday	3C	4C	1C	2C		8B	9B	12B		
Friday	7B	6B (9.30 – 10.55)		5B (11.05 – 12.30)		10B	11B	14B	15B	

SLOT 1	1A, 1B, 1C	One hour	SLOT 9	9A, 9B	One and half hour	Slot L1	3 hour slot
SLOT 2	2A, 2B, 2C	One hour	SLOT 10	10A, 10B	One and half hour	Slot L2	3 hour slot
SLOT 3	3A, 3B, 3C	One hour	SLOT 11	11A, 11B	One and half hour	Slot L3	3 hour slot
SLOT 4	4A, 4B, 4C	One hour	SLOT 12	12A, 12B	One and half hour	Slot L4	3 hour slot
SLOT 5	5A, 5B	One and half hour	SLOT 13	13A, 13B	One and half hour	Slot L5	3 hour slot
SLOT 6	6A, 6B	One and half hour	SLOT 14	14A, 14B	One and half hour	Slot L6	3 hour slot
SLOT 7	7A, 7B	Slot for quizzes	SLOT 15	15A, 15B	One and half hour	Slot LX	3 hour slot
SLOT 8	8A, 8B	One and half hour					

TIMETABLE FOR AUTUMN SEMESTER 2019-2020
III SEMESTER B.TECH (ELECTRICAL ENGINEERING)

Course No.	Course Name	Department	Lecture		Instructor
			Slot	Venue	
EE 201	Data Analysis	CSE, EE, ME	5A, 5B	23	Prof. Sudhanshu Shukla and Prof. Mahadeva Prasanna
HS 201	Economics	CSE, EE, ME	6A, 6B	23	Prof. Gopal Sharan Parashari
EE 203	Electronic devices	EE	2A, 2B, 2C, XC	117	Prof. Ruma Ghosh
EE205	Network theory	EE	1A, 1B, 1C 14A, 7A	117	Prof. Harish Pillai
EE 207	Signals and Systems	EE	4A, 4B, 4C	117	Prof. Naveen M. B
MA 201	Complex Analysis	EE	3A, 3B, 3C, 7B	117	Prof. Amlan K Barua
MA 203	Differential equations II	EE	3A, 3B, 3C, 7B	117	Prof. Shreedevi