

GENERAL SLOT PATTERN FOR IV SEMESTER B.TECH (COMPUTER SCIENCE ENGINEERING)

Day	8.30 – 9.25	9.30 – 10.25	10.35 – 11.30	11.35 – 12.30		2.30 – 3.55	4.00 – 5.25	5.30 – 6.55	7.00 – 8.25
Monday	1A	2A	3A	4A	Lunch break between 12.30 to 2.00	8A	9A	12A	13A
Tuesday	4B	1B	2B	3B		10A	11A	14A	15A
Wednesday	7A	5A (9.30 – 10.55)		6A (11.05 – 12.30)		X1	X2	X3	
Thursday	3C	4C	1C	2C		8B	9B	12B	13B
Friday	7B	6B (9.30 – 10.55)		5B (11.05 – 12.30)		10B	11B	14B	15B

SLOT 1	1A, 1B, 1C	One hour	SLOT 9	9A, 9B	One and half hour	Slot L1	3 hour slot
SLOT 2	2A, 2B, 2C	One hour	SLOT 10	10A, 10B	One and half hour	Slot L2	3 hour slot
SLOT 3	3A, 3B, 3C	One hour	SLOT 11	11A, 11B	One and half hour	Slot L3	3 hour slot
SLOT 4	4A, 4B, 4C	One hour	SLOT 12	12A, 12B	One and half hour	Slot L4	3 hour slot
SLOT 5	5A, 5B	One and half hour	SLOT 13	13A, 13B	One and half hour	Slot L5	3 hour slot
SLOT 6	6A, 6B	One and half hour	SLOT 14	14A, 14B	One and half hour	Slot L6	3 hour slot
SLOT 7	7A, 7B	Slot for quizzes	SLOT 15	15A, 15B	One and half hour	Slot LX	3 hour slot
SLOT 8	8A, 8B	One and half hour					

TIMETABLE FOR SPRING SEMESTER 2019-2020
IV SEMESTER B.TECH (COMPUTER SCIENCE ENGINEERING)

Course No.	Course Name	Department	Lecture		Instructor
			Slot	Venue	
CH301	Environmental_Studies	All	2	23	Dr. Tembe
EE214	Digital Circuits Lab	CSE, EE	8B, 9B (L3)	Electronics Lab	Dr. Ruma
CS204	Computer_Networks	CSE	3A, 3B, 5A(1Hr)	211	Dr. Siba
CS205	Design and Analysis of Algorithms	CSE	1A, 1B, 6A(1Hr)	115	Dr. Ramchandra
EE204	Digital Systems	CSE, EE	4	117	Dr. Ruma
CS202	Automata Theory	CSE	3C, 1C, 6B(1Hr)	115	Dr. Nagaraja
CS212	Computer Networks Lab	CSE	X1, X2, X3 (LX)	120	Dr. Siba