

GENERAL SLOT PATTERN FOR V SEMESTER B. TECH (ELECTRICAL ENGINEERING)

Day	8.30 – 9.15	9.30 – 10.15	10.30 – 11.15	11.30 – 12.15	Lunch break	2.30 – 3.15	3.30 – 4.15	4.30 – 5.15	5.30 – 6.15	6.30 – 7.15	7.30 – 8.15
Monday	1A	2A	3A	4A		15A	16A	7A	8A	9A	10A
Tuesday	3B	1B	6A	5A		10 B	11A	15B	12A	13A	14A
Wednesday	6B	5B				7B	9B	8B	16B	15C	12B
Thursday	2B	5C	1C	4B		14B	13B	9C	10C	16C	11B
Friday	3C	6C	2C	4C		11C	12C	7C	14C	8C	13C
Saturday		-----L1-----				-----L2-----					

SLOT 1	1A, 1B, 1C	45 minutes	SLOT 9	9A, 9B, 9C	45 minutes	SLOT L1	3 hours
SLOT 2	2A, 2B, 2C	45 minutes	SLOT 10	10A, 10B, 10C	45 minutes	SLOT L2	3 hours
SLOT 3	3A, 3B, 3C	45 minutes	SLOT 11	11A, 11B, 11C	45 minutes		
SLOT 4	4A, 4B, 4C	45 minutes	SLOT 12	12A, 12B, 12C	45 minutes		
SLOT 5	5A, 5B, 5C	45 minutes	SLOT 13	13A, 13B, 13C	45 minutes		
SLOT 6	6A, 6B, 6C	45 minutes	SLOT 14	14A, 14B, 14C	45 minutes		
SLOT 7	7A, 7B, 7C	45 minutes	SLOT 15	15A, 15B, 15C	45 minutes		
SLOT 8	8A, 8B, 8C	45 minutes	SLOT 16	16A, 16B, 16C	45 minutes		

TIMETABLE FOR AUTUMN SEMESTER 2020-2021
V SEMESTER B.TECH. (ELECTRICAL ENGINEERING)

Course No.	Course Name	Department	Lecture		Instructor	Prerequisite
			Slot	Venue		
CS 301	Computer Architecture	CSE	1	Room 117	Prof. Rajshekar K	
EE 321	Digital Signal Processing	EE	2	Room 119	Prof. Rajshekhar V Bhat	
EE 309	Introduction to Communication Systems	EE	3	Room 203	Prof. Naveen M B	
HS 303	Introduction to Literature	HSS	4	Room 203	Prof. Ridhima Tiwari	
HS 301	Philosophy	HSS		Room 207	Prof. Jolly Thomas	
EE 406	VLSI Design	EE	5	Room 203	Prof. Naveen K	
CS 305	Software Engineering	CSE	6	Room 119	Prof. Nikhil Hegde	Data Structures and Algorithms, Programming in C/C++/Java.
EE 405	Pattern Recognition and Machine Learning	EE	7	Room 115	Prof. S R Mahadeva Prasanna	
CH 403	Quantum Field Theory	CHY	9	Room 115	Prof. B. L. Tembe	
PH 402	Astrophysics for Engineers	PHY	10	Room 117	Prof. D Narasimha	
BB 401	Biomedical Instrumentation	BSBE	11	Room 115	Prof. Sudhanshu Shukla, Prof. Surya Pratap Singh, Prof. Neelkamal Mahanta	
MA 403	Introduction to Number Theory	MA	12	Room 117	Prof. N.S.N. Sastry	
MA 401	Numerical Linear Algebra	MA	13	Room 115	Prof. Amlan Barua	
CS 427	Mathematics for Data Science	EE	14	Room 117	Prof. Bharath B N	
EE 427	Stochastic Process	EE	15	Room 115	Prof. Tejas Bodas	
CS 421	Logic for Computer Science	CSE	16	Room 117	Prof. Ramchandra Phawade	Discrete Mathematics, Theory of Computation
CS 311	Computer Architecture Lab	CSE	L2		Prof. Rajshekar K	

