

GENERAL SLOT PATTERN FOR III SEMESTER B. TECH (ELECTRICAL ENGINEERING)

Day	9:30-10:15	10:30-11:15	11:30-12:15	12.30-1.15	Lunch break	3:30-4:15	4:15-5:00	
Monday	1A	2A	3A	5A		4A		
Tuesday	2B	3B	1B	5B			6A	
Wednesday*	(9-10) D3	(10-11) D1	(11-12) D2			(2-3) D4	(3-4) D5	(4-5) D6
Thursday	6B		4B	5C		3C	2C	
Friday	4C	6C	1C			6D		

***Doubt Sessions**

SLOT 1	1A,1B,1C	45 minutes
SLOT 2	2A,2B,2C	45 minutes
SLOT 3	3A,3B,3C	45 minutes
SLOT 4	4A,4B,4C	45 minutes
SLOT 5	5A,5B,5C	45 minutes
SLOT 6	6A,6B,6C,6D	45 minutes
SLOT D	D1,D2,D3,D4,D5,D6	1 hour

TIMETABLE FOR AUTUMN SEMESTER 2020-2021
III SEMESTER B. TECH (ELECTRICAL ENGINEERING)

Course No.	Course Name	Department	Lecture Slot	Venue	Instructor
EE 221	Introduction to Probability	EE	2,D2 (till midsem)	Room 115	Prof. Naveen M.B.
EE 229	Electronic Devices	EE	3,D3 (till midsem)	Room 117	Prof. Ruma Ghosh
EE 205	Network Theory	EE	4, D4	Room 117	Prof. Abhijit Kshirsagar
EE 210	Signals & Systems	EE	5, D5	Room 115	Prof. S.R.M.Prasanna
HS 201	Economics	HSS	1,D1	Room 115	Prof. Gopal Parashari
MA 201	Complex Analysis	MA	6,D6 (till midsem)	Room 115	Prof. Shreedevi Masuti
MA 203	Differential Equations	MA	6,D6 (post midsem)	Room 115	Prof. Dhriti Ranjan Dolai
EE 227	Data Analysis	EE	2,D2 (post midsem)	Room 115	Prof. Sudhanshu Shukla
EE 202	Analog Circuits	EE	3,D3 (post midsem)	Room 117	Prof. Naveen Kadayinti