

Dear Applicant,

Congratulations on being short-listed for ETA positions (Advt no. IITDh/Admin/Rectt. /08/2018-2019) in the Department of Electrical Engineering at IIT Dharwad.

The short-listing criteria is as follows:

- Age limit of 30 years
- M.Tech/ equivalent Master's degree/Integrated Engineering degree with at least first class (60% or equivalent CGPA).

*Note that you were short-listed based on the details filled in the application form. IIT Dharwad will verify your credentials and you may be liable for disqualification in case of any major discrepancies between the details entered in the application form and the original marks-cards/certificates.*

### **Time and Date for Test**

- **Date: 27<sup>th</sup> October 2018 (Saturday)**
- **Time: 9:00 a.m.**
- Venue: IIT Dharwad, P.B. Road, Dharwad, Karnataka, 580011.

### **Screening Procedure**

- The candidates will undergo a screening procedure covering topics from the syllabus listed below.
- Their performance is evaluated by an ETA selection committee formed by selected faculty members in the Department of Electrical Engineering at IIT Dharwad.

### **Syllabus**

- **Engineering Mathematics:**
  - Linear Algebra, Real Analysis/Calculus, Transform Theory: Fourier Transform, Laplace Transform, z-Transform.
- **Communication and Signal Processing**
  - Signals and Systems, Analog and Digital Communication, Information Theory
- **Control Systems**
  - Linear Control Systems, Nyquist plots, Bode plots, PD, PI and PID controllers, Feedback control systems
- **Electronic Devices and Mixed signal ASIC Design**
  - Electronic Devices, Analog Circuits, Digital Systems
- **Electrical Machines**
  - Synchronous Machines, DC Machine, Induction Motor, Transformers and Power Electronics